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many modern computer systems including homogeneous and heterogeneous architectures support shared memory in hardware in a shared memory system each of the processor cores may read and write to a single shared address space for a shared memory machine the memory consistency model defines the architecturally visible behavior of its memory system consistency definitions provide rules about loads and stores or memory reads and writes and how they act upon memory as part of supporting a memory consistency model many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up to date the goal of this primer is to provide readers with a basic understanding of consistency and coherence this understanding includes both the issues that must be solved as well as a variety of solutions we present both high level concepts as well as specific concrete examples from real world systems this second edition reflects a decade of advancements since the first edition and includes among other more modest changes two new chapters one on consistency and coherence for non cpu accelerators with a focus on gpus and one that points to formal work and tools on consistency and coherence

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many modern computer systems and most multicore chips chip multiprocessors support shared memory in hardware in a shared memory system each of the processor cores may read and write to a single shared address space for a shared memory machine the memory consistency model defines the architecturally visible behavior of its memory system consistency definitions provide rules about loads and stores or memory reads and writes and how they act upon memory as part of supporting a memory consistency model many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up to date the goal of this primer is to provide readers with a basic understanding of consistency and coherence this understanding includes both the issues that must be solved as well as a variety of solutions we present both highlevel concepts as well as specific concrete examples from real world systems table of contents preface introduction to consistency and coherence coherence basics memory consistency motivation and sequential consistency total store order and the x86 memory model relaxed memory consistency coherence protocols snooping coherence protocols directory coherence protocols advanced topics in coherence author biographies

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